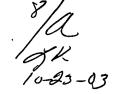


## THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re the Application of:

TI-31354

Serge Lasserre

Art Unit: 2188

Serial No:

09/932,381

Examiner: Reginald G. Bragdon

Filed:

August 17, 2001

Conf. No.: 4434

For:

Local Memory With Indicator Bits to Support Concurrent DMA and CPLACCESS ED

## **AMENDMENT PURSUANT TO 37 CFR 1.111**

AUG 2 8 2003

Technology Center 2100

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(a)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on August 22, 2003.

Pohin E Barrum

Dear Sir:

Applicant has received the Office Action mailed March 25, 2003, which rejected Claims 1-12. Please amend the above-identified patent application as follows: